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PATENT

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Date of Signature and Deposit: December 14, 2005, Keith M. Baxter, Reg. No. 31,233

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/083,814  
Filed: February 27, 2002  
Applicant(s): Joseph Francis Mann et al.  
Title: Processor with Versatile External Memory Interface  
Art Unit: 2116  
Examiner: Chen, Tse W.  
Docket No.: 01AB162 (IPB) 97745

RESPONSE TO OFFICE ACTION DATED SEPTEMBER 14, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated September 24, 2005, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.